



IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

H-300-08

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TC 1700

In re Patent Application of

Y. OKAMOTO et al

Serial No. 09/922,656

Group Art Unit: 1756

Filed: August 7, 2001

Examiner: Nicole M. Barreca

For: PROCESS FOR FABRICATING SEMICONDUCTOR INTEGRATED CIRCUIT
DEVICE, AND EXPOSING SYSTEM AND MASK INSPECTING
METHOD TO BE USED IN THE PROCESS

PETITION FOR EXTENSION OF TIME

Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

November 3, 2003

Sir:

It is respectfully requested that a One-month Extension of Time, to and including November 1, 2003, be granted in which to respond to the Office Action dated August 1, 2003 in the above-identified application.

Our Credit Card Payment Form in the amount of \$110.00 is attached in payment of the appropriate fee.

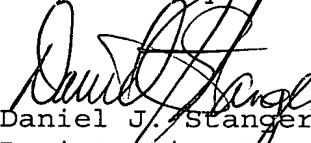
The Commissioner is hereby authorized to charge any additional payment due, or to credit any overpayment, to Deposit Account No. 50-1417.

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Respectfully submitted,


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Date: November 3, 2003